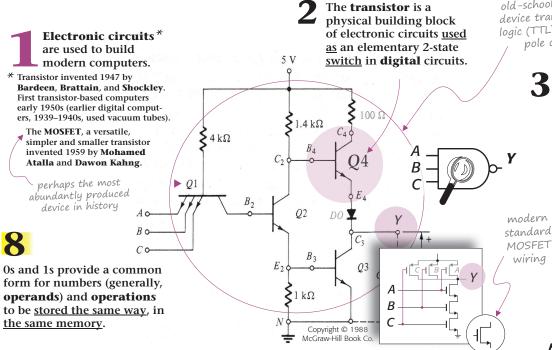
cs704fig\_slide0.cdr Sunday, January 21, 2024 8:05:00 AM Color profile: Disabled

Composite Default screen

## Physical Device to Code



A resulting **binary code** can be interpreted as a <u>number</u>, <u>expressed in the binary system</u>, to indicate a device's operational state, a stored value, or a numeric coding of an item, such as a text character.

6 Sequences of 0s and 1s can encode <u>switching states</u> of a digital device to indicate operational positions during a computation *or* a stored info.

old-school bipolar junction device transistor-transistor logic (TTL) wiring + totem-, pole output circuit fun names

The iconic circuit in the figure implements a simple **digital logic** device (3-input *NAND* gate) via five electronic switches (transistors Q1-4 and a diode).

Source: Millman & Grabel, *Microelectronics* (2nd ed, pp. 243-47), transistor logic technology circa 1966-85 from my undergrad electronics textbook.

- NAND gate, a building block of digital logic, is a logical\* switch that turns OFF if all inputs are ON (follows NOT-AND logic).
- \* Action based on a *Boolean* operation, such as *AND*, OR, or XOR. Physical features of transistor devices determine how to wire them to perform the logic.
- Any two symbols may encode the two states of a switch; 0/1 are useful (see why next).

Term coined -~ 1947 by John Tukey **010101** Bit (<u>b</u>inary dig<u>it</u>)

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